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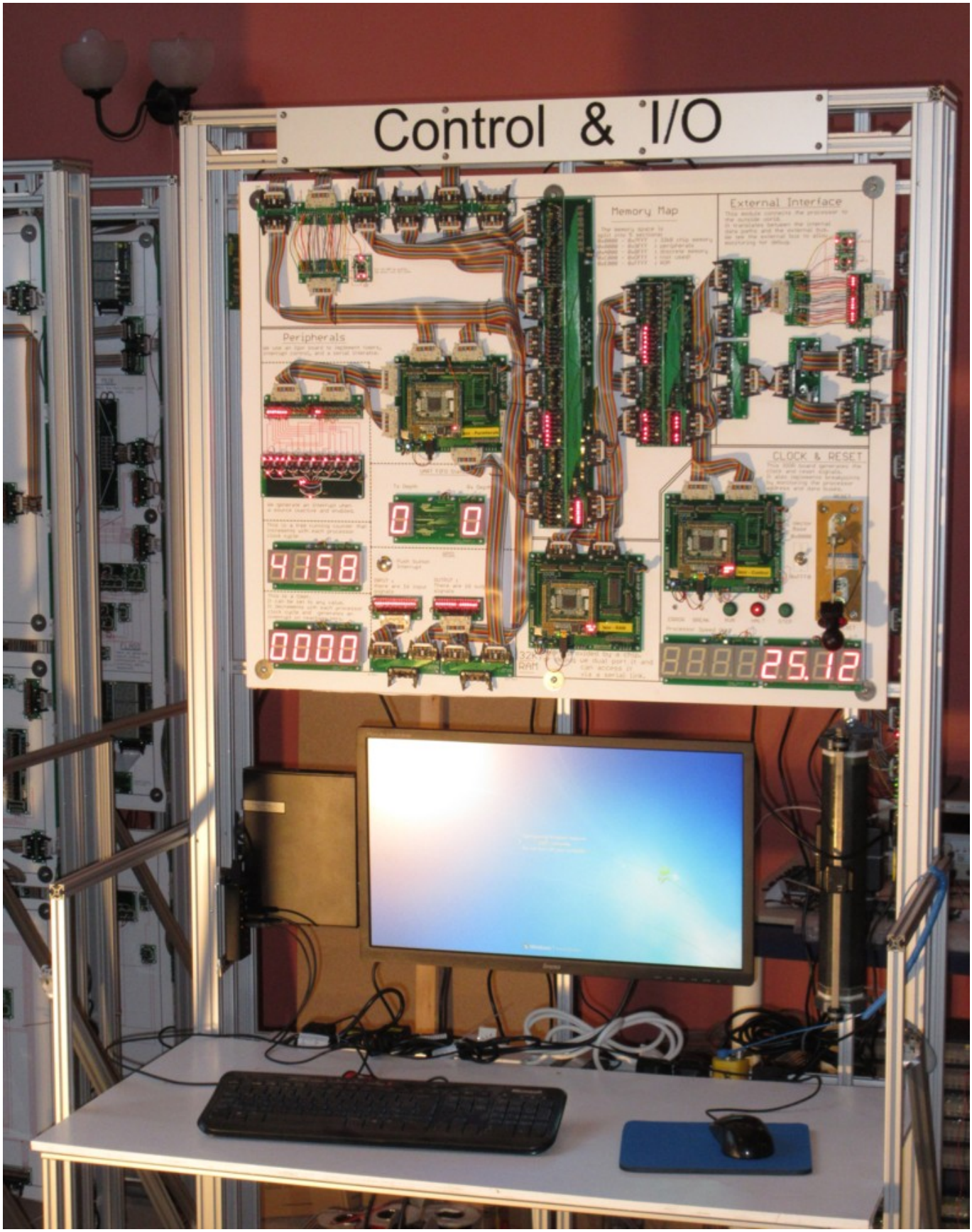
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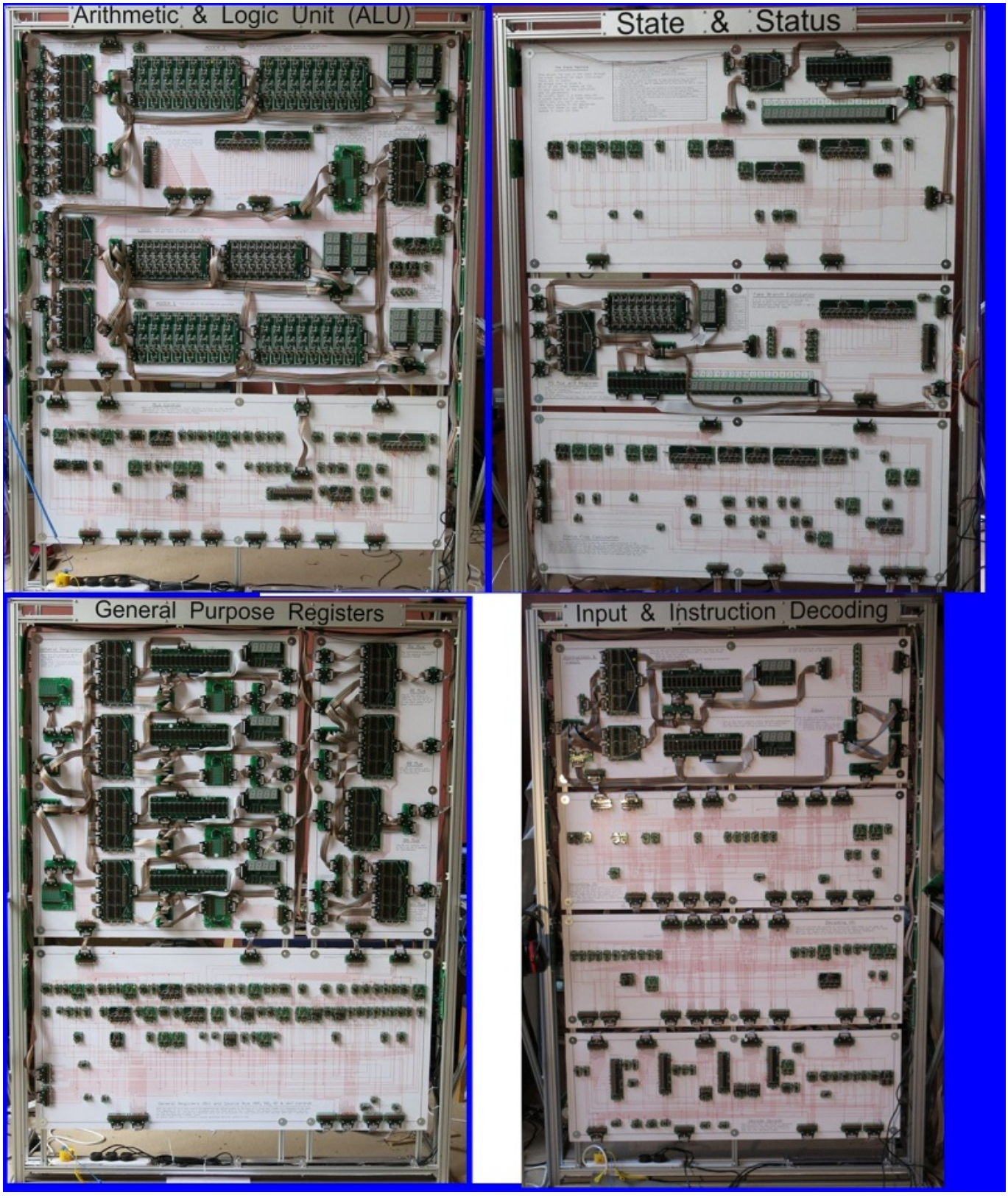
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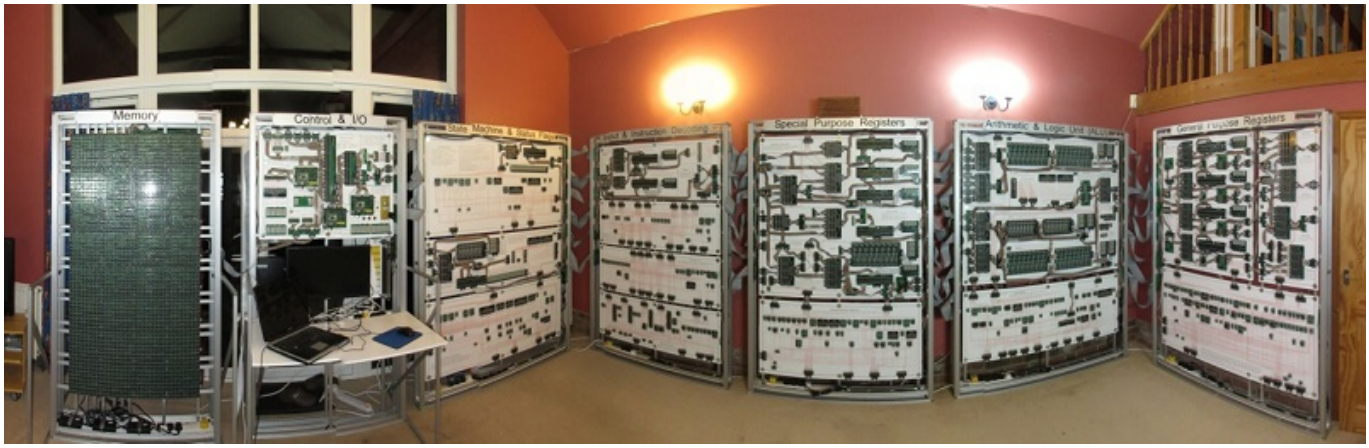
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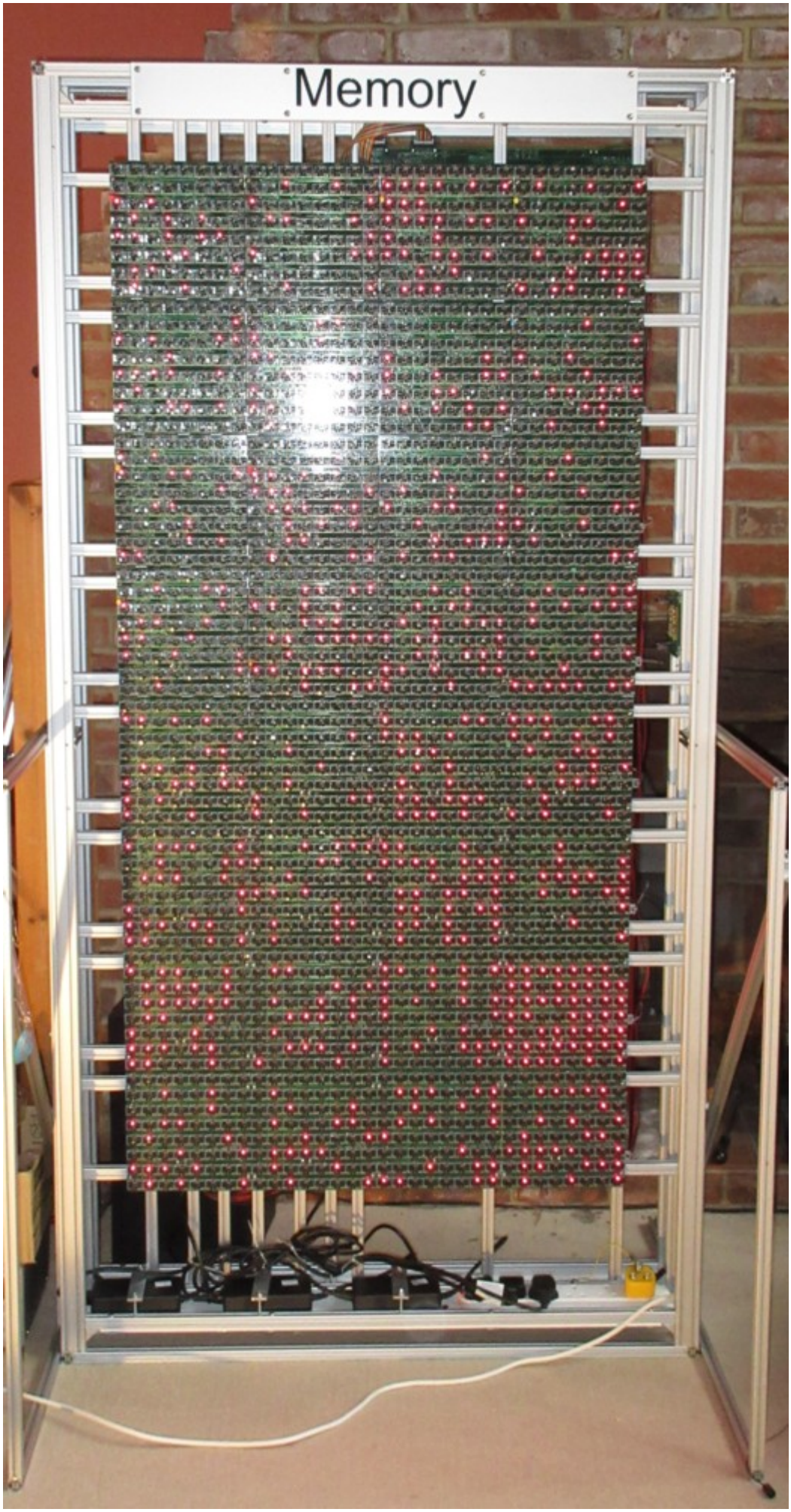
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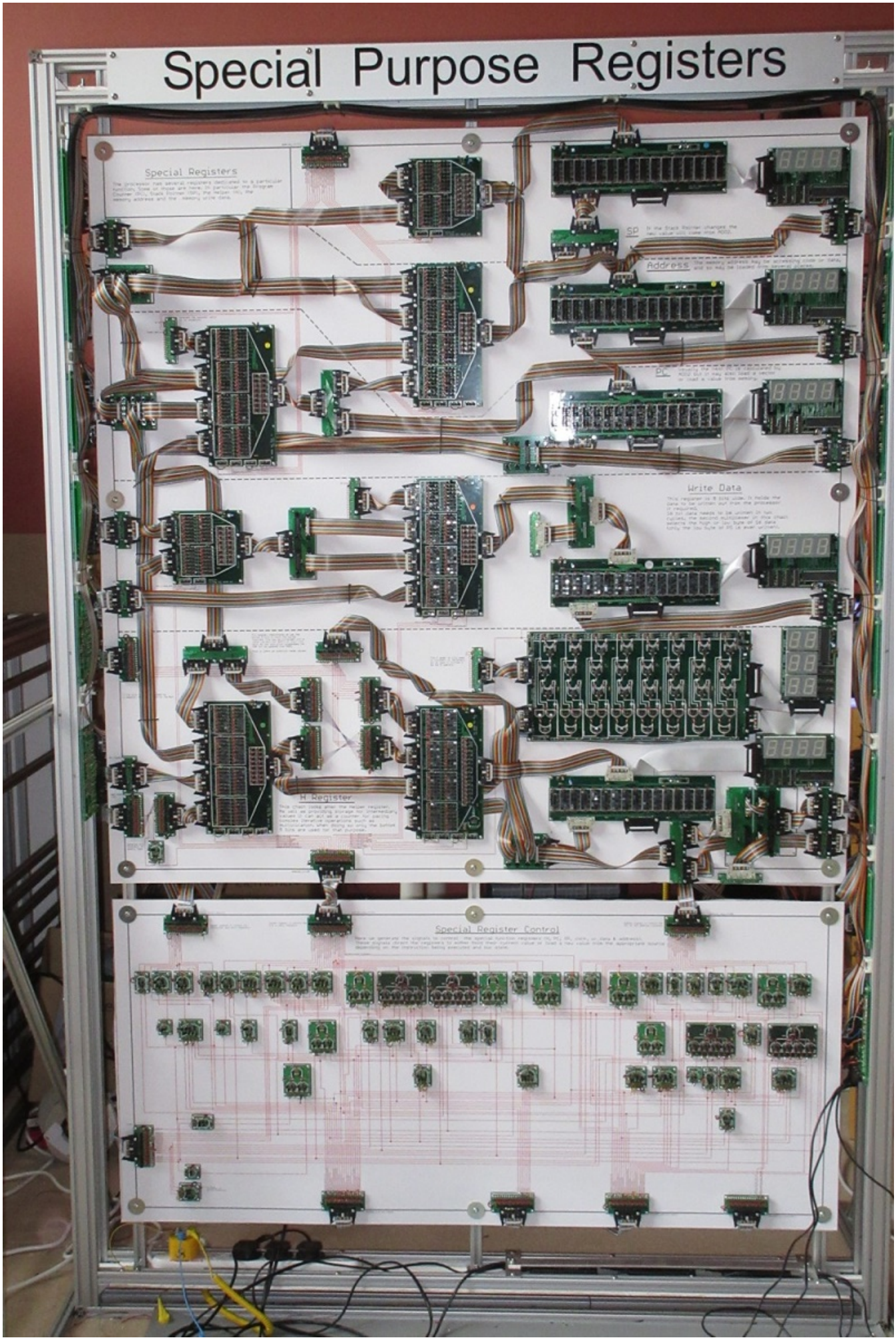
8 Bit Adder/Subtractor


Control & I/O
Memory Map
External Interface
Peripherals
CLOCK & RESET
4158
0000
25.12


Arithmetic & Logic Unit (ALU)
State & Status
General Purpose Registers
Input & Instruction Decoding


Memory
Control & I/O
Special Purpose Registers
Arithmetic & Logic Unit (ALU)


Memory


Special Purpose Registers
Special Registers
SP
Address
PC
Write Data
H Register
Special Register Control



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