

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ : □ □ □ □ □ □

98 □□□□□□ □□□□□□□□

[illegible]

IT 0000000000 000 000000 00 000000 00000 000 27 000000 Raymond Computer 0000 00 000000 000000
.000 00000 00000 00 00000 00000000 00 0000000 0 00000 000000 00 000000 KVM 000000 000000 000000 000000 0

[illegible]

: □□□□ □□□□

The diagram shows a horizontal shift register with 10 bits. The first 5 bits are labeled 1 through 5 from left to right. The last 5 bits are labeled 6 through 10 from left to right. Each bit is represented by a square box. Arrows indicate the direction of data flow from left to right between adjacent bits.

: □ □ □ □ □ □ □ □ □ □

05:40 - 30/04/1398

• ☐ ☐ ☐ ☐ ☐

98 000000 - 000000 - 000000 00000000 - 000000 000000

11

<https://www.shabakeh-mag.com/news/iran/15787/%D8%AA%D9%85%D8%A7%D8%B4%D8%A7-%DA%A9%D9%86%DB%8C%D8%AF-%D8%A8%D8%A7%D8%B2%D8%AF%DB%8C%D8%AF->

%D8%A7%D8%B2-%D8%BA%D8%B1%D9%81%D9%87-
%D8%B1%D8%A7%DB%8C%D9%85%D9%86%D8%AF-
%D8%B1%D8%A7%DB%8C%D8%A7%D9%86%D9%87-%D8%AF%D8%B1-
%D9%86%D9%85%D8%A7%DB%8C%D8%B4%DA%AF%D8%A7%D9%87-
%D8%A7%D9%84%DA%A9%D8%A7%D9%85%D9%BE-98