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A diagram of a 10-bit shift register. It consists of two horizontal rows of five rectangular cells each. The top row is labeled 'Q9' through 'Q4' from left to right. The bottom row is labeled 'Q3' through 'Q0' from left to right. Arrows indicate data flow: from 'Q4' to 'Q3', from 'Q3' to 'Q2', from 'Q2' to 'Q1', and from 'Q1' to 'Q0'. There is also a feedback loop from 'Q0' back to 'Q4'.

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