

□□□□ □□□□ □□□□□□ □□□□□□□□ □□□□ : □□□□ □□□□□□
□□□□ □□□□ □□□□□□□□ □□□□ □□ □□□□□□

[illegible][illegible]

: □ □ □ □ □ □ □ □

A diagram of a 10-bit shift register. It consists of two horizontal rows of five rectangular cells each. The top row is labeled with bits 9, 8, 7, 6, and 5 from left to right. The bottom row is labeled with bits 4, 3, 2, 1, and 0 from left to right. The cells are connected horizontally, and there is a feedback loop from the output of the bottom row back to the input of the top row.

[illegible]

19:50 - 07/05/1397

•

97 97 2018 2018

<https://www.shabakeh-mag.com/news/iran/13545/%D8%AA%D9%85%D8%A7%D8%B4%D8%A7-%D8%A9%D9%86%DB%8C%D8%AF-%D9%85%D8%B9%D8%B1%D9%81%DB%8C-%D8%AC%D8%AF%DB%8C%D8%AF%D8%AA%D8%B1%DB%8C%D9%86-%D9%85%D8%AD%D8%B5%D9%88%D9%84%D8%A7%D8%AA-%D8%B4%D8%B1%DA%A9%D8%AA-%D8%A7%D9%85%D9%86-%D8%A7%D9%81%D8%B2%D8%A7%D8%B1-%D8%A7%D8%B2-%D8%B2%D8%A8%D8%A7%D9%86-%D9%85%D8%AF%DB%8C%D8%B1%D8%B9%D8%A7%D9%85%D9%84-%D8%A7%DB%8C%D9%86-%D8%B4%D8%B1%DA%A9%D8%AA>