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[illegible]

The diagram shows a horizontal shift register with 10 bits. The top row of bits is labeled with the hexadecimal value 0000000000. The bottom row of bits is labeled with the hexadecimal value 0000000000. The register is divided into two 5-bit sections by a vertical line in the middle. The left section has a '0' input on the left and a 'Q' output on the right. The right section has a '0' input on the left and a 'Q' output on the right.

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<https://www.shabakeh-mag.com/news/iran/10307/%D8%AA%D9%85%D8%A7%D8%B4%D8%A7-%D8%A9%D9%86%DB%8C%D8%AF-%DA%AF%D9%81%D8%AA-%D9%88-%DA%AF%D9%88-%D8%A8%D8%A7-%D9%85%D8%AF%DB%8C%D8%B1-%D9%81%D8%B1%D9%88%D8%B4-%D9%87%D9%84%D8%AF%DB%8C%D9%86%DA%AF-%D9%81%D8%B1%D8%A8%DB%8C%D9%86>