



၀၀၀၀ ၀၀ ၀၀ ၀၀၀၀ ၀၀၀၀ .၀၀၀ ၀၀၀၀၀ ၀၀၀၀ ၀၀ ၀၀ ၀၀၀ Super Micro ၀၀၀၀ ၀၀၀၀၀ ၀၀၀၀ ၀၀ ၀၀၀ ၀၀၀၀၀ ၀၀၀၀၀ ၀၀၀ CNBC ၀၀၀၀ ၀၀ ၀၀၀၀ ၀၀ ၀၀၀ .၀၀၀ ၀၀၀၀၀ ၀၀ ၀၀၀၀ ၀၀၀၀ ၀.၀ ၀၀၀၀ ၀၀၀၀၀၀၀ ၀ ၀၀၀၀ ၀၀၀၀၀ ၀၀ ၀၀၀၀၀ ၀၀၀၀၀၀၀ ၀၀၀ ၀၀၀၀၀၀၀ ၀၀၀၀၀၀၀၀ ၀၀၀၀၀၀၀ » : [၀၀၀ ၀၀၀၀၀](#) ၀၀၀၀၀၀ ၀၀၀ ၀၀ ၀၀၀ ၀၀၀၀ ၀၀၀၀၀၀၀ ၀၀၀၀၀ ၀၀၀၀ ၀၀ ၀၀ ၀၀၀၀၀ ၀၀ ၀၀၀၀၀ ၀၀၀၀၀၀ ၀၀ ၀၀၀၀၀ ၀၀၀၀၀ ၀၀၀၀၀၀ ၀၀၀၀၀၀၀ ၀၀ ၀၀၀၀ ၀၀၀၀ ၀၀ ၀၀၀၀၀၀၀၀ ၀၀ ၀၀၀၀၀၀ ၀၀၀၀ ၀၀ .၀၀၀ ၀၀၀၀၀၀၀ ၀၀၀၀၀၀ ၀၀ ၀၀ ၀၀၀၀ ၀(၀ ၀၀၀၀၀၀) ၀၀၀၀ ၀၀၀ ၀၀၀၀၀ ၀၀၀ ၀၀၀ ၀၀ ၀၀၀ .၀၀၀၀ ၀၀၀၀၀

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Super Micro

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A diagram of a 10-bit shift register. It consists of 10 flip-flops arranged in a horizontal row, labeled Q9, Q8, Q7, Q6, Q5, Q4, Q3, Q2, Q1, and Q0 from left to right. Each flip-flop has a clock input (triangle symbol) connected to a common clock line. The output of each flip-flop is connected to the input of the next flip-flop to its right. The output of the rightmost flip-flop, Q0, is connected to the input of the leftmost flip-flop, Q9, forming a closed loop.

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QUESTION

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