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 (segmenting) 0000 0000 000 0 0000000000 00000000 00 0000 000000 0000 0000 .0000 00000 00000
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0000 000000 000000 00 VRF 00 (VLAN) 000000 0000 0000 00 00000000 00 00 0000000 000000

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A diagram of a 10-bit shift register. It consists of two groups of five rectangular cells, each representing a bit. The cells are arranged in a single horizontal row. The first group of five cells is on the left, followed by a small gap, and then the second group of five cells on the right. All cells are empty, indicating they are initialized to 0.

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مقاله

<https://www.shabakeh-mag.com/networking-technology/16815/%E2%80%8C%D9%85%D8%B9%D9%85%D8%A7%D8%B1-%D8%B4%D8%A8%DA%A9%D9%87-%D9%88-%D8%B7%D8%B1%D8%A7%D8%AD-%D8%B4%D8%A8%DA%A9%D9%87-%DA%86%D9%87-%D8%AA%D9%81%D8%A7%D9%88%D8%AA%DB%8C-%D8%A8%D8%A7-%DB%8C%DA%A9%D8%AF%DB%8C%DA%AF%D8%B1-%D8%AF%D8%A7%D8%B1%D9%86%D8%AF>