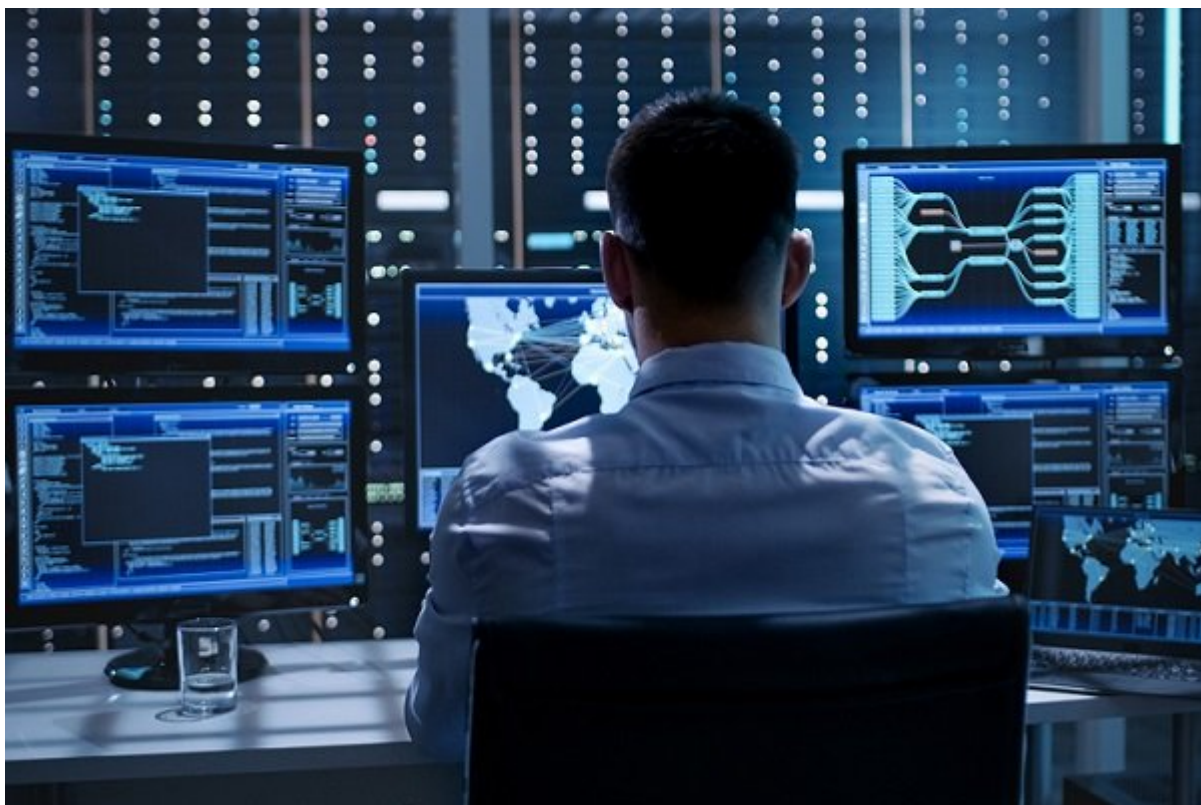


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Diagram illustrating the subtraction of two 16-bit numbers using 8-bit registers. The first number is 0000 0000 0000 0000 and the second is 0000 0000 0000 0001. The result is 1111 1111 1111 1111.

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<https://www.shabakeh-mag.com/networking-technology/16751/%DA%86%DA%AF%D9%88%D9%86%D9%87-%DB%8C%DA%A9-%D9%85%D9%87%D9%86%D8%AF%D8%B3-%D8%A7%D9%85%D9%86%DB%8C%D8%AA-%D8%B4%D8%A8%DA%A9%D9%87-%D8%B4%D9%88%DB%8C%D9%85%D8%9F>

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