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[illegible]

C7000 .**Windows** **8.1** **64-bit** **OS** **ADSL** **Modem** **Router** **Nighthawk** **USB** **3.0** **Port** **DOCSIS** **3.0** **Power** **Supply** **Unit** **PSU** **R7000** **Nighthawk**

256-QAM 128-QAM .DOCSIS 600-QAM 5-QAM 1300-QAM 2.4-QAM 600-QAM 280-QAM MIMO 3x3

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A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells. Above each cell is an input label: A, B, C, D, E, F, G, H, I, J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, J. The cells are connected in a chain, with the output of one cell being the input to the next cell to its right.

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[AC1900](#) - [□□□□ □□□□](#) - [ADSL](#) - [802.11ac](#) - [MIMO □□□□](#) - [□□□□ □□□□](#) - [Netgear C7000 Nighthawk](#)

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