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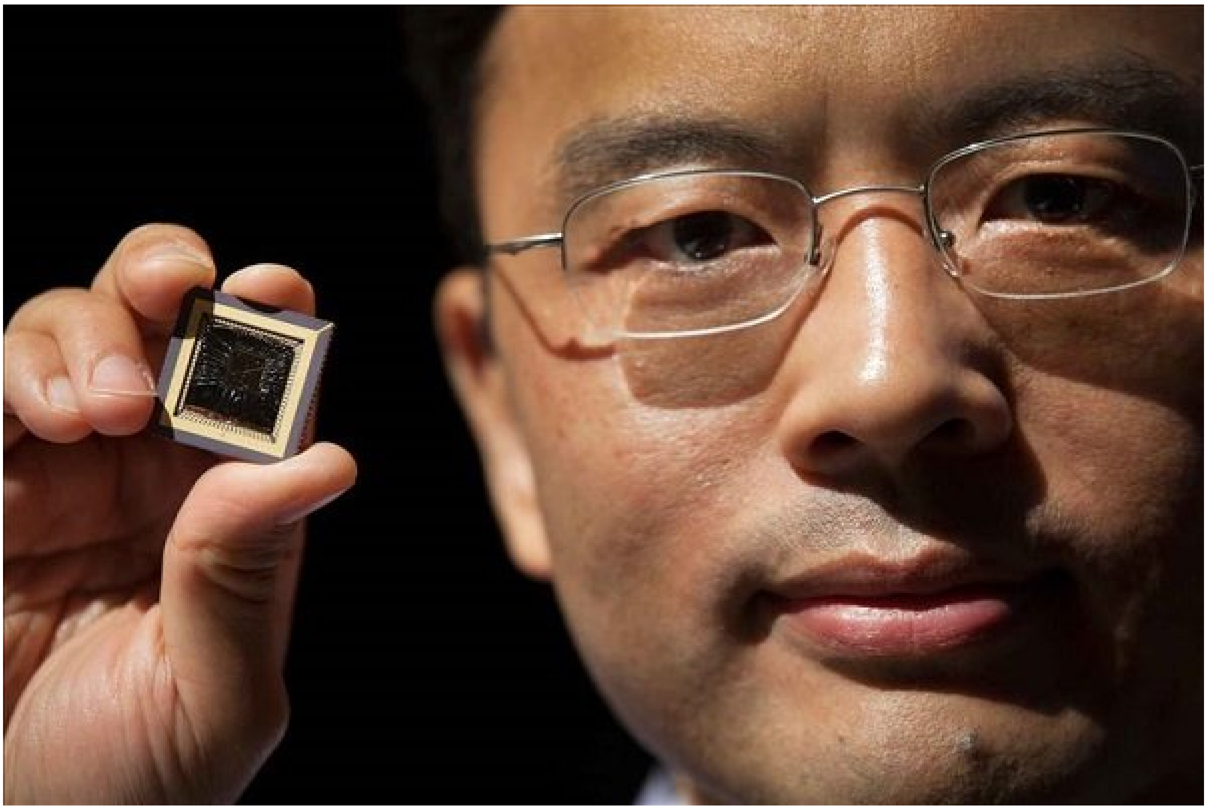
[illegible]

[illegible]



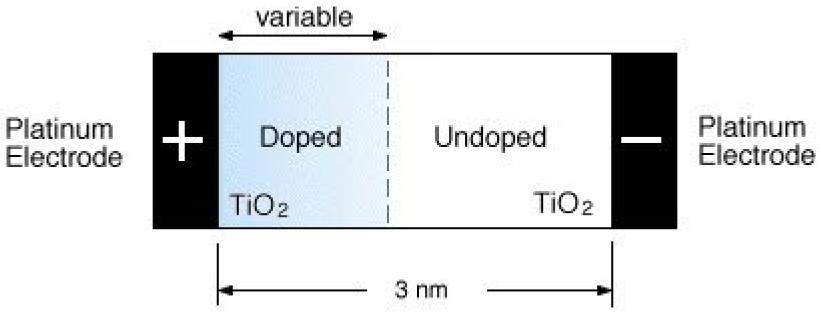
[illegible]

[illegible] (Wei Lu) [illegible] «[illegible].» [illegible].

[illegible]

[illegible] 1 [illegible].

[illegible]



[illegible] 1. [illegible]

[illegible]

[illegible] 1971 [illegible] 2008 [illegible] 120 [illegible] 10 [illegible]

[illegible]

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- [illegible]
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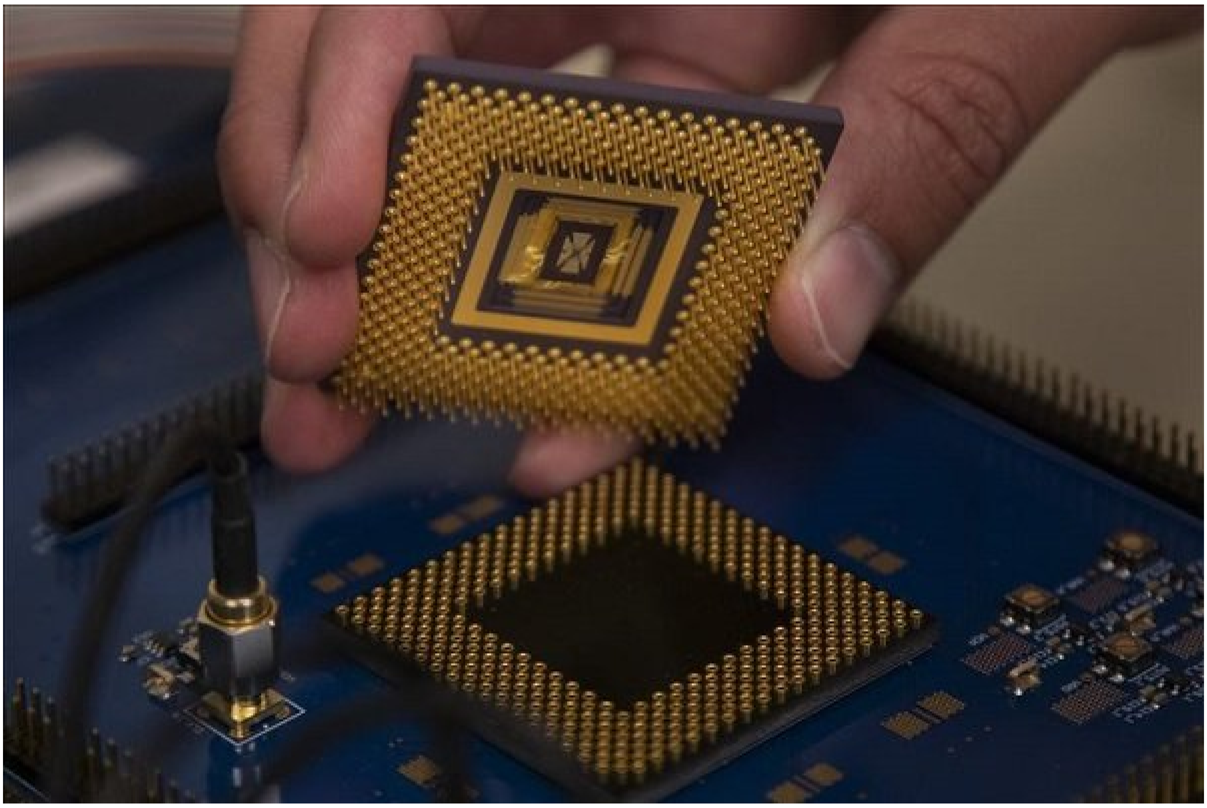
[illegible] 10 [illegible] 100 [illegible] 10 [illegible] 100 [illegible] 5800 [illegible] (2 [illegible]).



[illegible] 2. [illegible]

[illegible] (input) [illegible] (feature) [illegible]

[illegible]

(natural resistance) [illegible]

[illegible]

[illegible]:

- [illegible] (Perceptron) [illegible] 100 [illegible].
- [illegible] Sparse coding [illegible] 100 [illegible].
- [illegible] 94.6 [illegible].

[illegible]: A fully integrated reprogrammable memristor–CMOS system for efficient multiply–accumulate operations [illegible] «[illegible]» [illegible].

[illegible]:
[illegible]
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[illegible]
[illegible]: https://www.shabakeh-mag.com/information-feature/16154/%D8%A7%D9%88%D9%84%DB%8C%D9%86-%DA%A9%D8%A7%D9%85%D9%BE%DB%8C%D9%88%D8%AA%D8%B1-%D9%85%D9%85%D8%B1%DB%8C%D8%B3%D8%AA%D9%88%D8%B1%DB%8C-

%D8%A8%D8%B1%D9%86%D8%A7%D9%85%D9%87%E2%80%8C%D9%BE%D8%B0%DB%8C%D8%B1%D8%8C-%D9%BE%D8%B1%D8%AF%D8%A7%D8%B2%D8%B4-%D9%87%D9%88%D8%B4-%D9%85%D8%B5%D9%86%D9%88%D8%B9%DB%8C-%D9%81%D8%A7%D8%B1%D8%BA-%D8%A7%D8%B2-%D8%A7%D8%A8%D8%B1