



: □□□□ □□□□

A diagram of a 10-bit shift register. It consists of ten rectangular cells arranged in a horizontal row. The first cell on the left is labeled '1' and contains the value '1'. The second cell is labeled '2' and contains '0'. The third cell is labeled '3' and contains '1'. The fourth cell is labeled '4' and contains '0'. The fifth cell is labeled '5' and contains '1'. The sixth cell is labeled '6' and contains '0'. The seventh cell is labeled '7' and contains '1'. The eighth cell is labeled '8' and contains '0'. The ninth cell is labeled '9' and contains '1'. The tenth cell on the right is labeled '10' and contains '0'. A feedback line originates from the output of the 10th cell and connects back to the input of the 1st cell.

:

17:10 - 12/07/1398

: □□□□

2019 年 01 月 01 日 - 2019 年 01 月 01 日 - 2019 年 01 月 01 日 - 2019 年 01 月 01 日 - 2019 年 01 月 01 日

<https://www.shabakeh-mag.com/ads-report/16132/%DA%AF%D9%81%D8%AA-%D9%88-%DA%AF%D9%88-%D8%A8%D8%A7-%D9%85%D8%AF%DB%8C%D8%B1-%D9%81%D8%B1%D9%88%D8%B4-%D8%B3%D8%A7%D8%B2%D9%85%D8%A7%D9%86%DB%8C-%D8%B4%D8%B1%DA%A9%D8%AA-%D9%BE%D8%A7%D8%B1%D8%B3-%D8%A7%D8%B1%D8%AA%D8%A8%D8%A7%D8%B7-%D8%AF%D8%B1-%D9%86%D9%85%D8%A7%DB%8C%D8%B4%DA%AF%D8%A7%D9%87-%D8%A7%DB%8C%D9%BE%D8%A7%D8%B3-2019>