





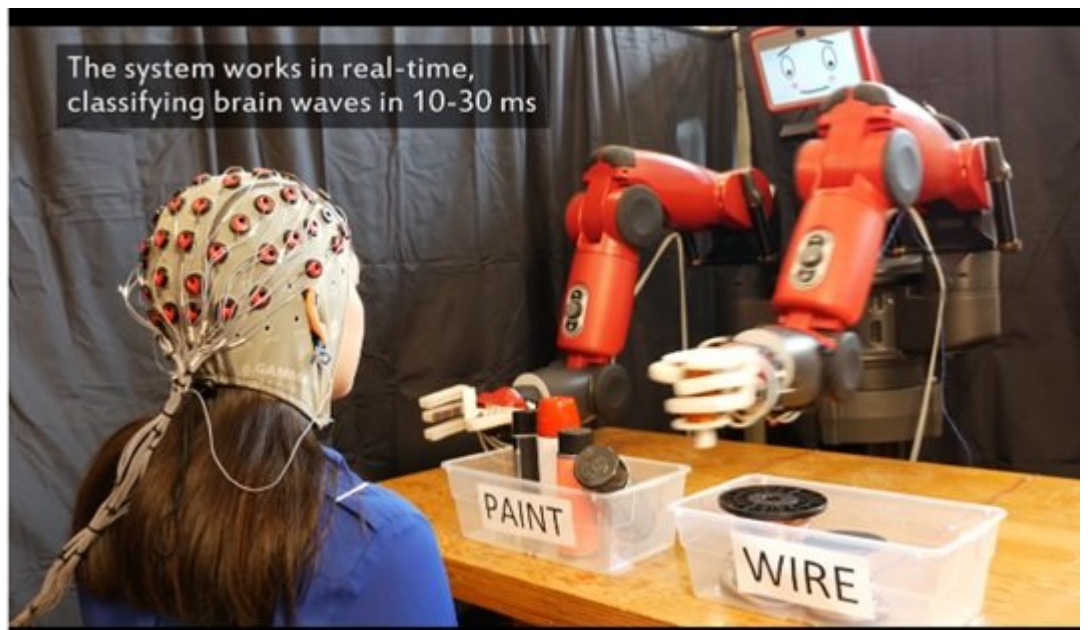
[illegible][illegible]

Diagram illustrating the mapping of a 16-bit instruction to a 32-bit register. The instruction is split into a 12-bit opcode and a 4-bit register number. The opcode is placed in the high-order 12 bits of the register, and the register number is placed in the low-order 4 bits. The register is labeled 'R' and 'R[31:0]'.

19:17 - 20/12/1395

•

MIT - CSAIL - Baxter - [Robotics](#) - [Machine Learning](#) - [Robotics](#) - [Machine Learning](#)

---

<https://www.shabakeh-mag.com/robot/7151>: ربات های کوچک