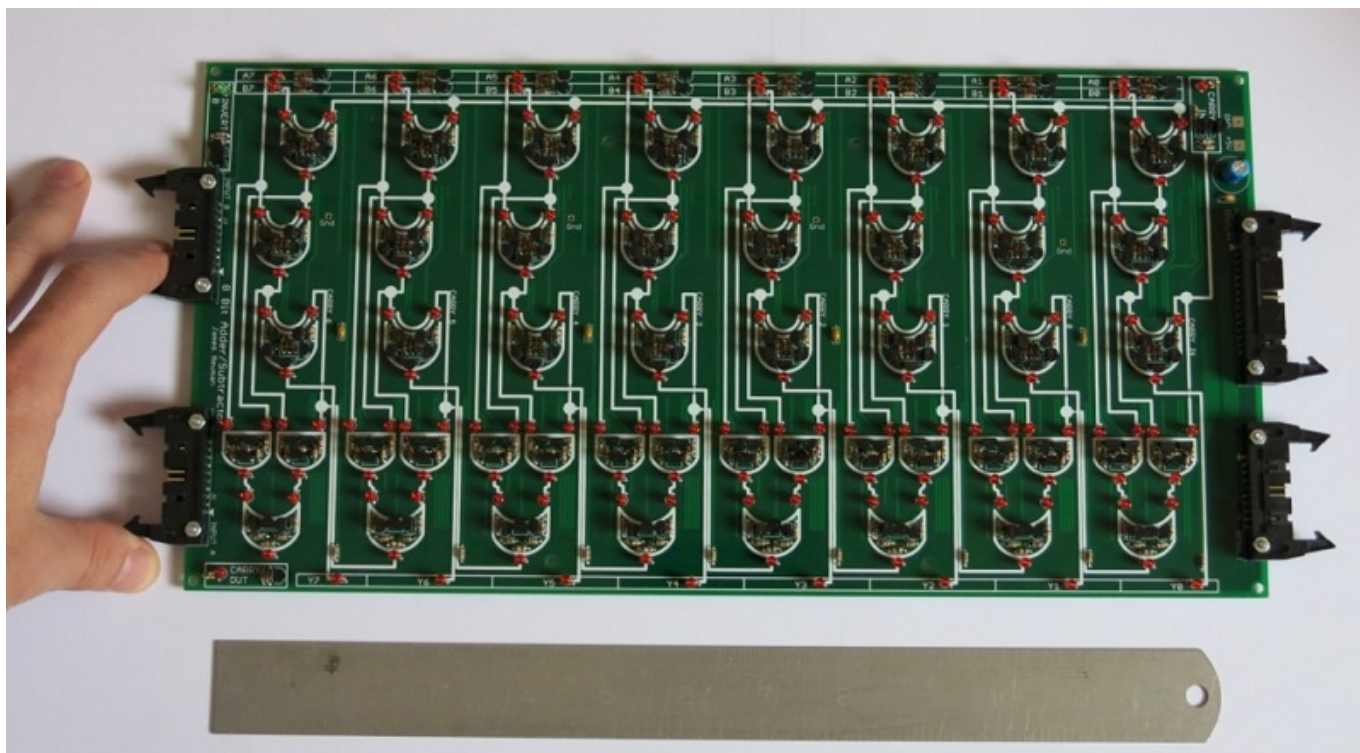


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Control & I/O

Memory Map

The memory map is as follows:

Address	Device	Size
0x0000	8x1711	256K on-chip memory
0x0000	8x1711	peripherals
0x0000	8x1711	32K on-chip memory
0x0000	8x1711	32K on-chip memory
0x0000	8x1711	32K on-chip memory

External Interface

This module connects the processor to the outside world. It provides a means for the external data bus and the external bus. It also provides a means for the external bus to connect to the external world.

Peripherals

Use an on-chip block to implement various peripheral functions, such as a serial interface.

CLOCK & RESET

This module generates the clock and reset signals for the processor. It also provides a means for the external bus to connect to the external world.

32K RAM

32K RAM

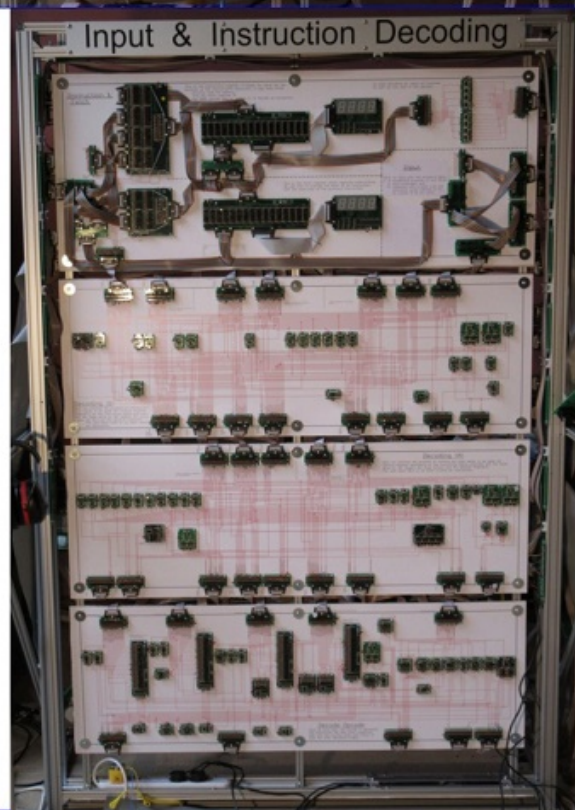
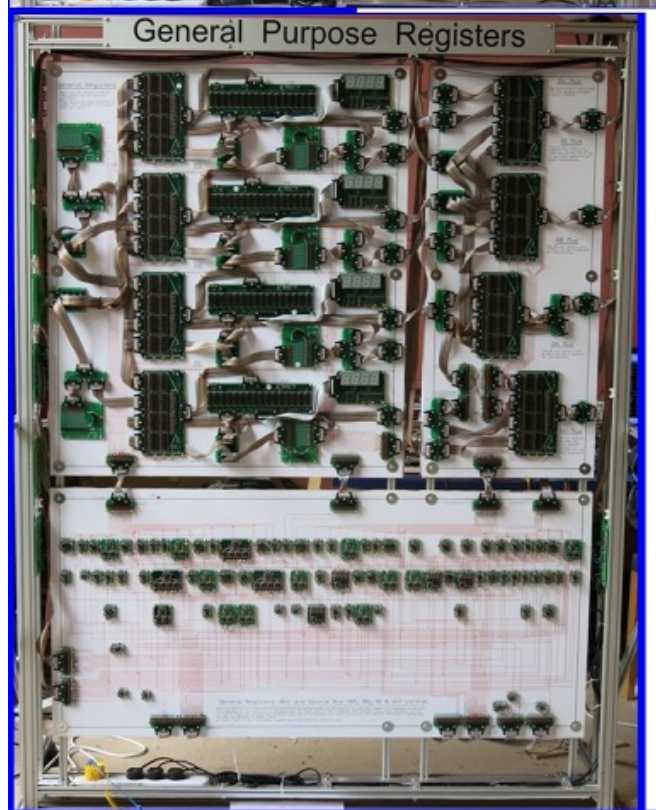
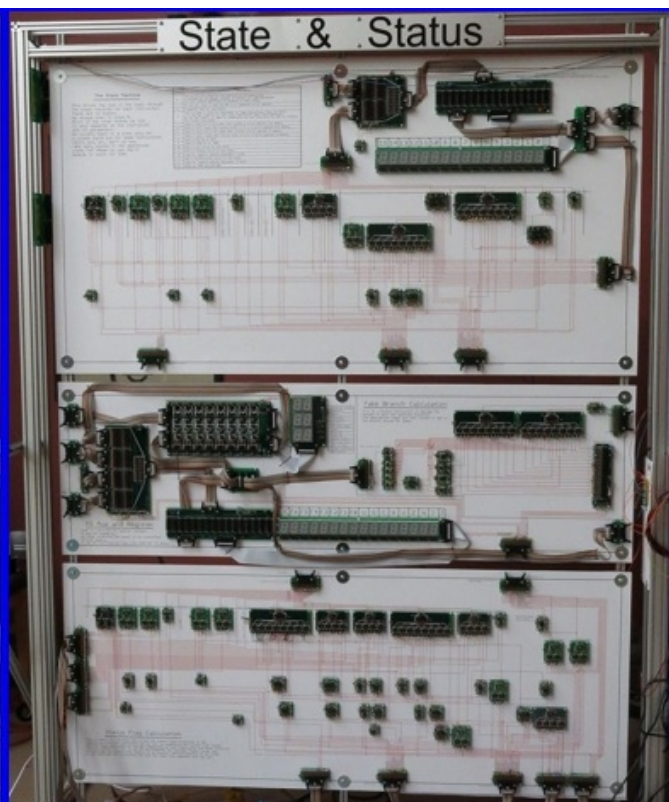
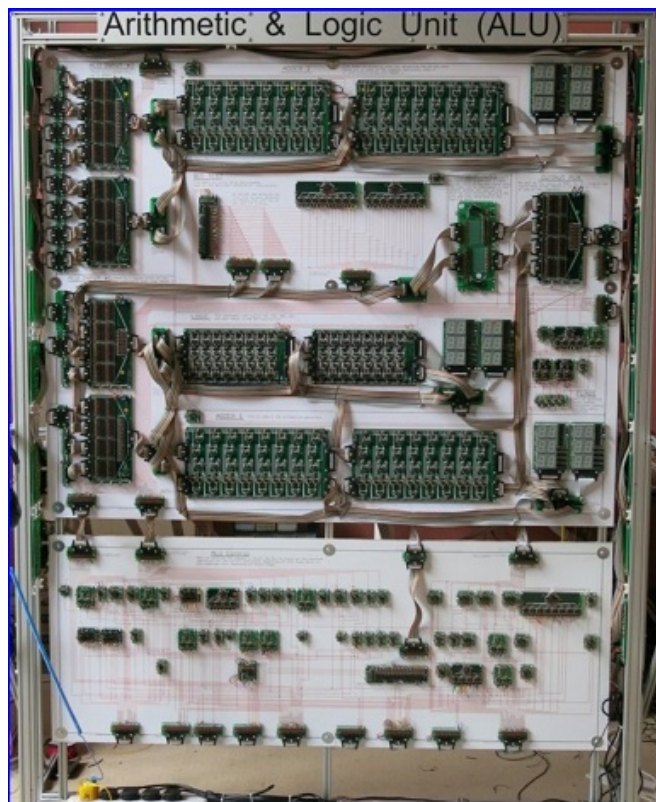
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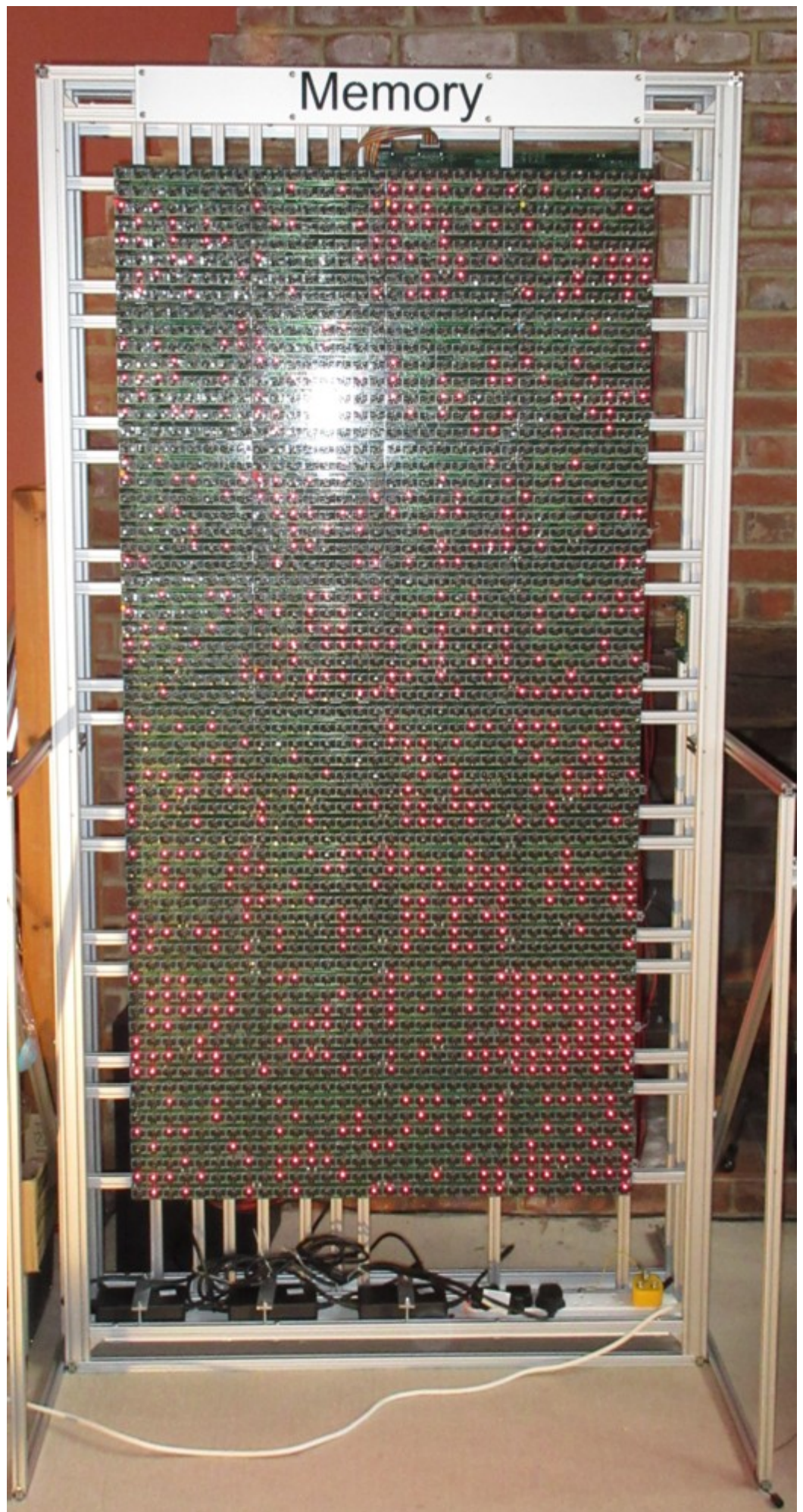
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Special Purpose Registers

Special Registers

The processor has several registers dedicated to a particular function. Some of those are here. In particular the Program Counter (PC), Stack Pointer (SP), the Index register, the memory address and the memory write data.

Is the final balance changed the new value will come from ACC2.

Address

Write Data

This register is 8 bits wide. It holds the data to be written out from the processor if required.

If the data needs to be written in two cycles, the second multiplexer in the chip selects the high or low byte of the data (only the low byte of 32 is ever written).

H Register

Stack chain looks after the register file and all the processing, storage for any values it can act as a counter for a complex iterative operations, such as multiplication, when doing an only in

Special Register Control



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