

[illegible]

[illegible]

0000 00 0000 00000 000000 (0000 00 00000 000000) 000000 00 0000 0 000 00000 0000000 000000000000
 00 00000000 00 000 000 00000 000 00000 .000 00 00000 00 (00000000 00 00000000) 00000000 000 0000
 000000 000000000000000000 0000000 00000000000 0 0000000000000 0000000000000 00000000 00 00000000 000
 00000000 0000 0000000 000000 0000 000 00 00 000000 0000000 00000000 000000 00 00 0000 00000000 .000000
 000000 00 00000000 00 00000000 00 0 000000 0000000 000000 0000 00 00000000000 0 0000000000000000
 37 00 25 000 00 0000 000000000000 0000 00 0000000000000 0 000000 00000000 00 00 00000 0 000000000
 000000 00 00000000000000 000 000000 00 000000 00000000000 0000 0000000 0000 000000 000 00 .000000 000000 0000
 .000000 000000 0000 0000000000000 0000000000 000000 0 0000000000 00 00 000000 0 00 00000000



0000	00000000	0	0000	00000000	00000000	0000000000	00	000000
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A diagram of a 10-bit shift register. It consists of 10 empty rectangular boxes arranged in a horizontal row, connected by a line underneath. The first four boxes are on the left, followed by a gap, and then the last four boxes on the right.

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<https://www.shabakeh-mag.com/information-feature/iot/9354/%DA%86%DA%AF%D9%88%D9>:□□□□

%86%D9%87-

[%D8%A7%DB%8C%D9%86%D8%AA%D8%B1%D9%86%D8%AA%E2%80%8C%D8%A7%D8%B4%D](#)

[B%8C%D8%A7-%D8%AF%D9%86%DB%8C%D8%A7%DB%8C-](#)

[%D9%BE%DB%8C%D8%B1%D8%A7%D9%85%D9%88%D9%86-%D9%85%D8%A7-](#)

[%D8%B1%D8%A7-%D9%85%D8%AA%D8%AD%D9%88%D9%84-](#)

%D8%AE%D9%88%D8%A7%D9%87%D8%AF%DA%A9%D8%B1%D8%AF-

%D9%88%DB%8C%D8%AF%DB%8C%D9%88